

FIG. 1 (PRIOR ART)

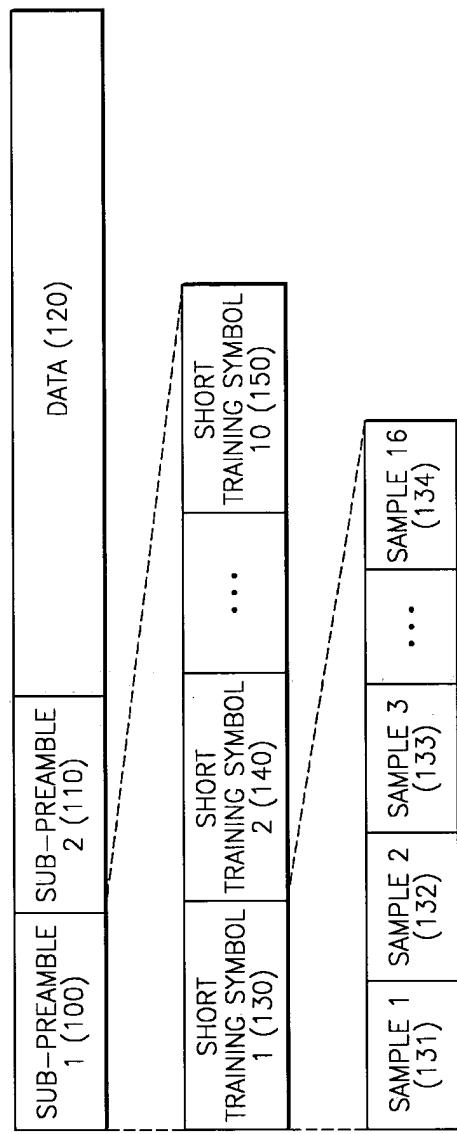


FIG. 2 (PRIOR ART)

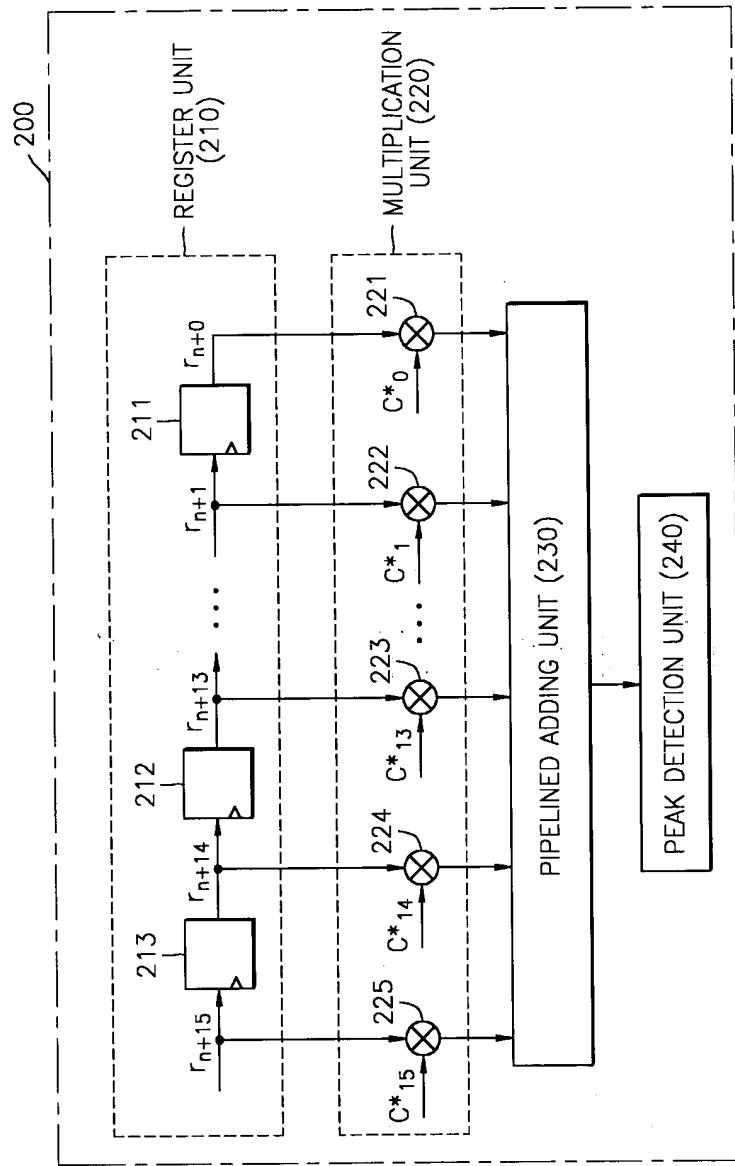


FIG. 3

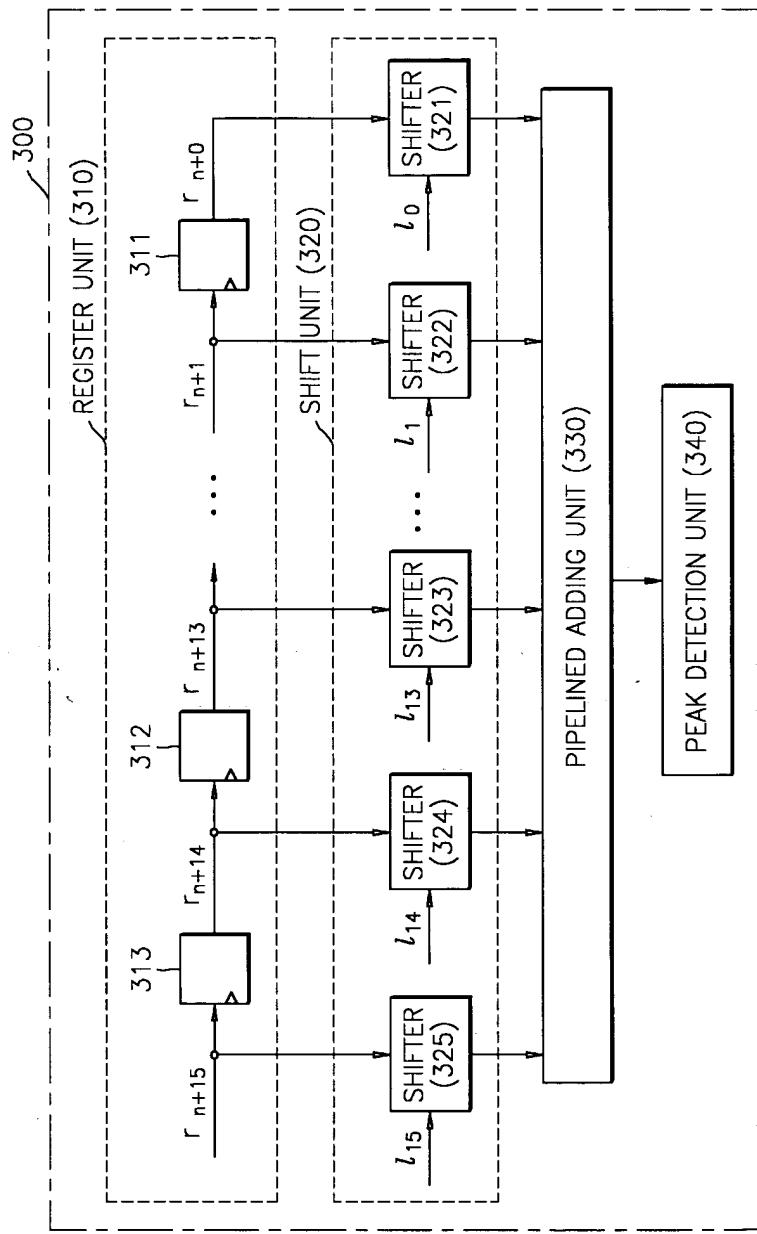


FIG. 4

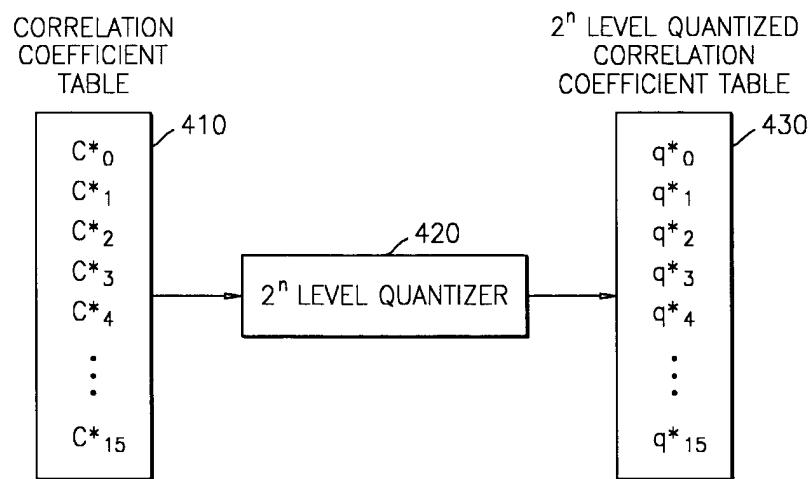


FIG. 5

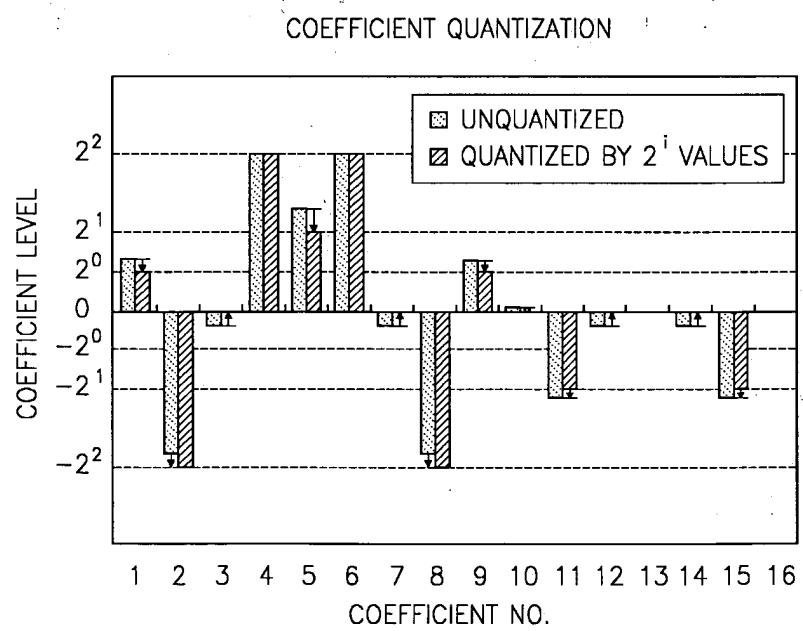


FIG. 6

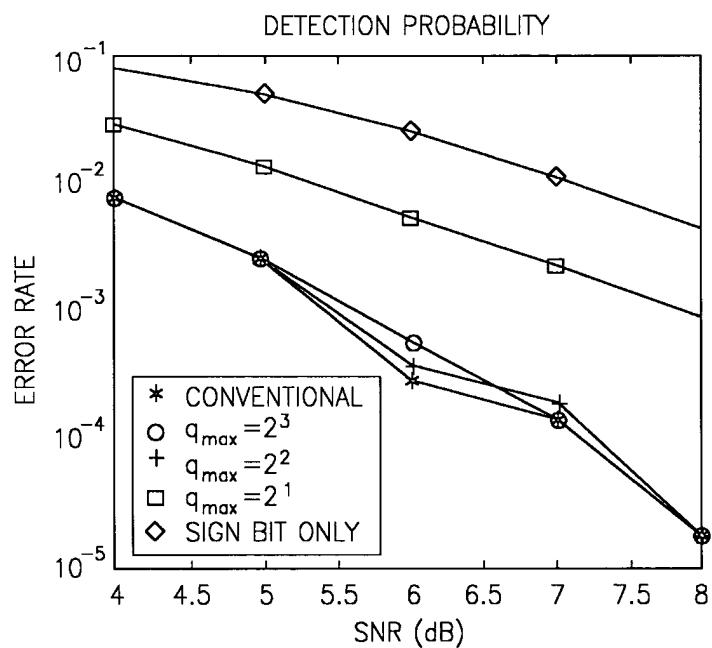


FIG. 7

| ITEMS             | NUMBER OF GATES     |                 |
|-------------------|---------------------|-----------------|
|                   | CONVENTIONAL SYSTEM | PROPOSED SYSTEM |
| MULTIPLIERS (8x8) | 16                  | 0               |
| REGISTER (8-bit)  | 15                  | 15              |
| ADDER             | 1 (16-bit)          | 1 (11-bit)      |
| TOTAL GATE NUMBER | 9495 (100%)         | 978 (10.3%)     |